Project student will be supporting:

Designing Energy-efficient and Secure Hardware

Sponsor:

Ajay Joshi

Hours per week

10

Paid?

UROP

Job description

I lead the Integrated Circuits and Systems Group at Boston University and I have multiple undergraduate researcher openings. Projects that have these openings include designing secure processor architectures and digital circuits, designing energy-efficient hardware accelerators for machine learning based applications, and designing energy-efficient communication circuits and architectures for heterogeneous computing systems. Each undergraduate student will work closely with a graduate student, who will also serve as the mentor.

Qualifications:

- Completed EK307, EC311, EC327, EC413, EC513 (preferable), and EC571 (preferable)
- Comfortable with designing hardware in Verilog
- Comfortable with programming in C and Python