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Avionics Test Engineer

City, State: **Huntsville, AL**
 Requisition Number: **10015**
 Date Posted: **6/29/2009**

Country: **United States**
 Employment Type: **Full Time**

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Description

As a winner of the 2008 and 2009 Best Places to Work Award, Jacobs ESTS Group offers a partnership in which you can grow personally and professionally with the advantages of strong leadership, competitive compensation, and rewarding career paths. Come join the team whose work is destined to have a long-range effect on future generations!

Provide detailed technical support for avionics test planning as it relates to the Avionics Test Events to be performed at the Ares I System Integration Laboratory (SIL), which includes both avionics hardware and flight software, with an emphasis on software development and test for verification.

Provide input to and actively support the primary developer of the Ares I Level III Integrated Avionics System Test Plan, specifically in the area of Test Case Development. Will be called upon to independently produce planning documentation and test procedures to ensure proper coverage of requirements closure through test activities. Perform the above mentioned tasks for all follow-on plans to be implemented in the Ares I SIL.

Coordinate and communicate regularly with the element-led avionics function test groups to ensure that the combination of test efforts made by each of these independent groups will eventually lead to a properly tested and verified Integrated Avionics Systems (IAS). Support regular meetings pertaining to working group, planning, integrated testing, avionics, software, safety, and other Ares Test related issues as needed.

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Education, Training & Experience

BS degree in Electrical Engineering or related field from an ABET accredited institution with a minimum of 5 to 8 years applicable experience is required. Experience in test preparation/planning, test conduct, procedure development, procedure execution and troubleshooting of avionics hardware/software problems. Strong background with computer software interaction and usage is essential. Hardware in the loop (HWIL) testing facilities experience preferable. The ability to comprehend custom programmed software packages and interact with such is vital. Thorough understanding of flight hardware verification testing is desired. Experience in complex simulation-based test beds and a background in verification testing of flight hardware necessary.

Physical Requirements

Requires sitting for extended periods of time in meetings with peers, management, and with our client at NASA facilities to discuss technical issues (10%). Also, requires sitting for extended periods of time at a desk to write reports and perform engineering tasks (80%). Requires ability to walk between floors and multiple buildings at NASA and Jacobs facilities (10%). Stairs or elevators can gain access.

Work Environment

Office environment. Requires ability to provide clear, concise, accurate and timely communication, both verbally and in writing (100%). Requires ability to interact professionally with co-workers, management, and client (100%). Requires travel in the domestic USA (<15%).

Equipment and Machines

Requires ability to operate a personal computer, a telephone, fax machine, copier, calculator, and other general office equipment (100%).

Attendance

Normal workday is from 7:30 a.m. to 4:30 p.m., Monday thru Friday. Minimal overtime may be required (10%) to meet schedule milestones and to support technical demands of the job. Regular attendance is a necessity and adequate arrangements for delegating duties during absences are required.


Other Essential Functions

Ability to work independently with minimal supervision, and to make rational decisions, and to exercise good judgment (100%). Grooming and dress must be appropriate for the position and must not impose a safety risk/hazard to the employee or others.

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