EC772 - VLSI Graduate Design Project
Spring 2013 - Mon-Wed 6-8 PM in PHO 201
Number of credits: 4, Prerequisites: EC 571

Course objective
By the end of the course, you should be able to design a digital CMOS chip based on given specifications

Staff Information
Instructor:
  Name: Allyn Hubbard
  Office address: PHO 329
  Office phone number: 617-353-2815
  E-mail address: aeh@bu.edu
  Office hours: PHO 329, Thursday 1-5:30pm or by appointment

Lab Assistant:
  Name: Roy Sun
  E-mail address: rsun@bu.edu
  Office hours: PHO 314, TBD

Course Resources
Reference Books:
  - Digital VLSI Chip Design with Cadence and Synopsys CAD Tools, E. Brunvand, Pearson

Announcements, course material and other useful links: Will be posted on Blackboard (http://blackboard.bu.edu)

Evaluation
Grading criterion: Three Milestones = 10% Each, Presentation = 10%, Final Report = 60%
*Final Report Due May 7, 2013

Course Policy
• I and W grades: As per University policy.